



Windfoil: Workload- Independent Formation of Power and Thermal Islands for SoC Characterization

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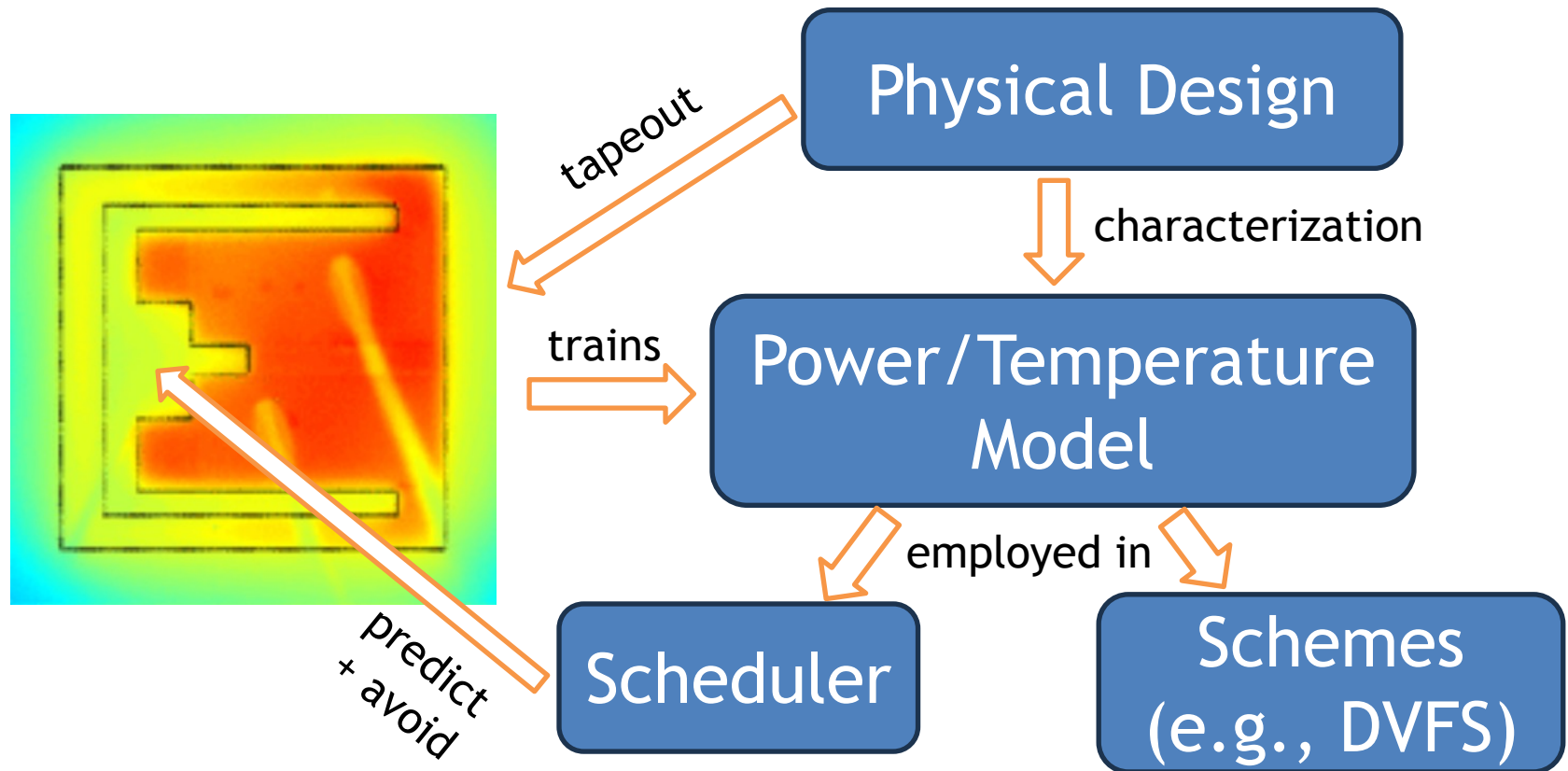
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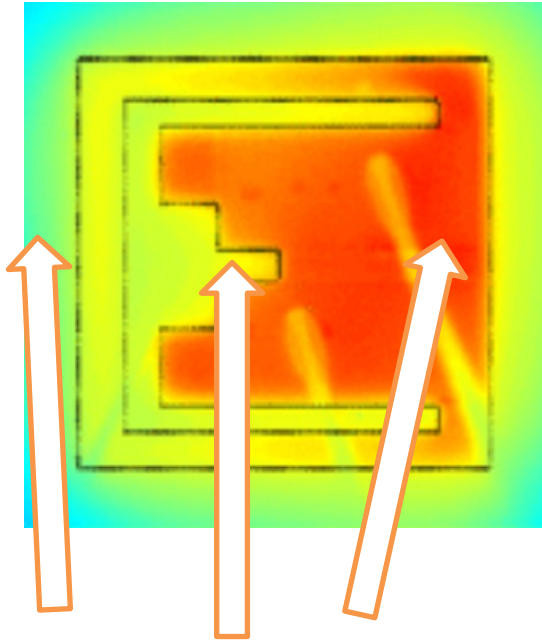


Power and thermal modeling



Management schemes require **accurately** modeling the power and temperature with sufficient **abstraction**.

Power and thermal modeling



Performance events (PMU)

Compute power from activity using weights!

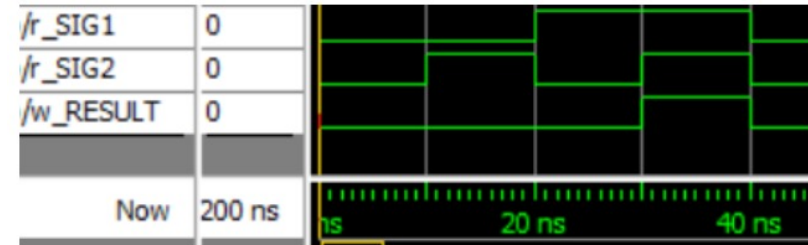
$$P = \sum \beta_i * \text{count}\{event_i\}$$

Train based on a “representative” workload

Design-time approaches

Per-cycle
power trace
(not runtime)

Accuracy
Ground truth



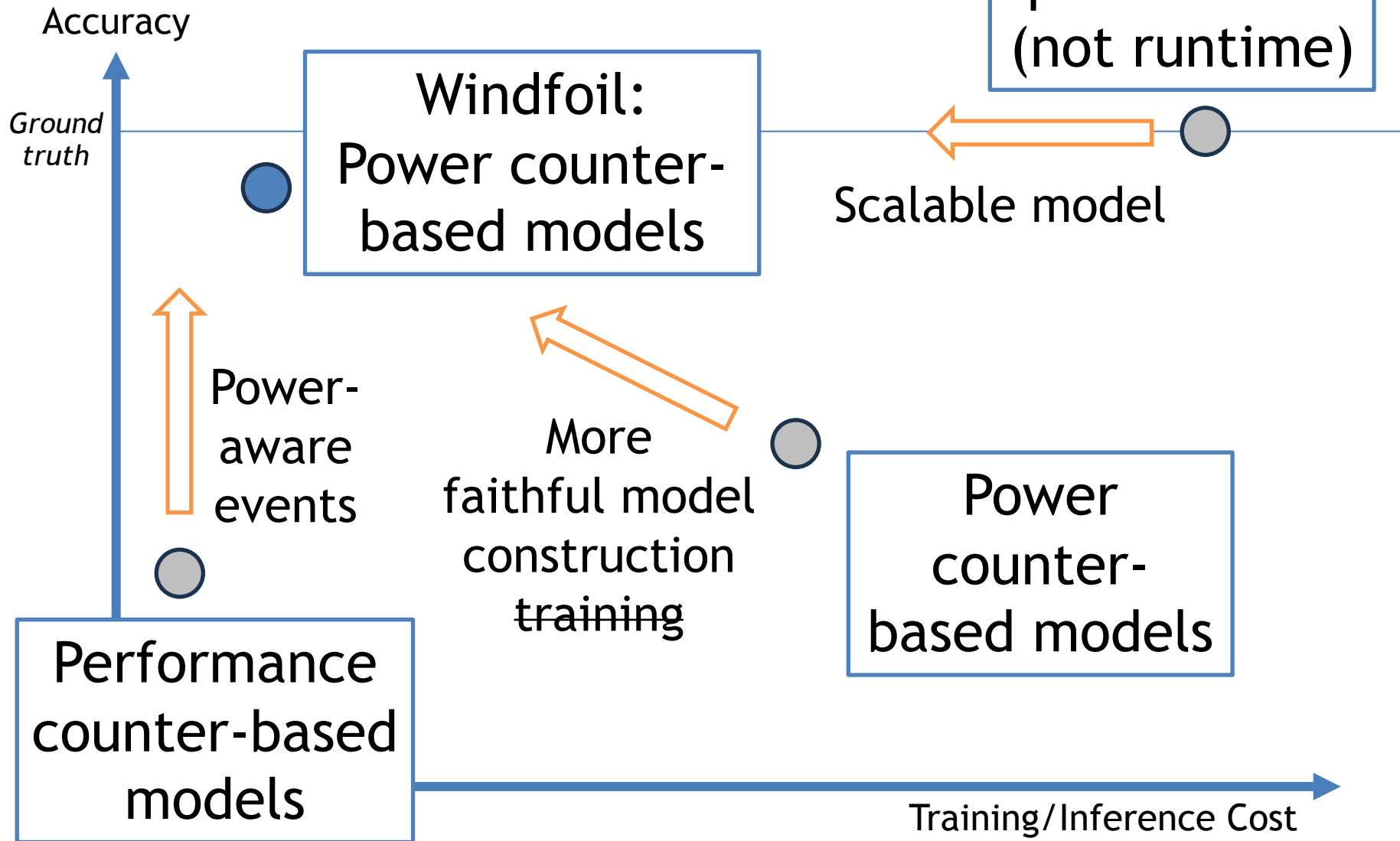
$$P = \sum \beta_i * \text{count}\{event_i\}$$

Power
counter-
based models

Performance
counter-based
models

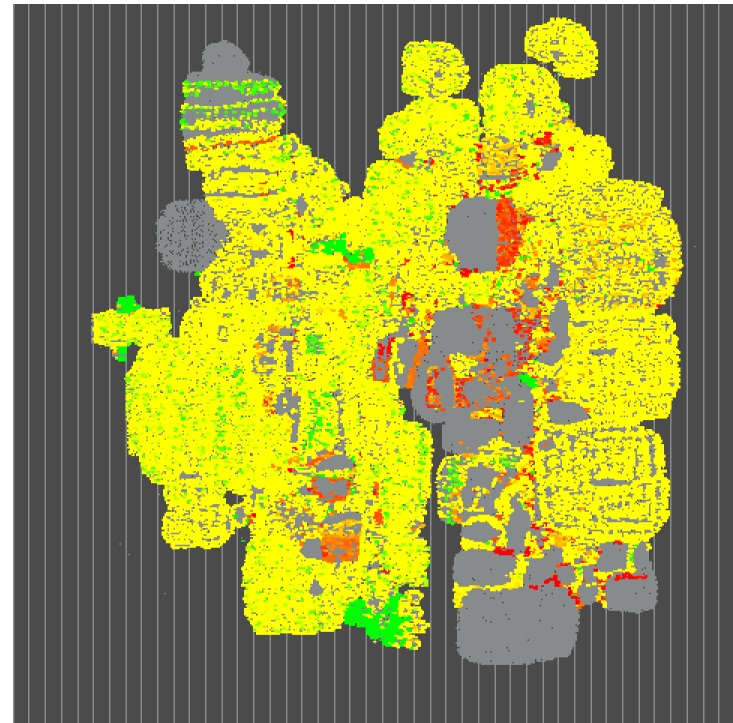
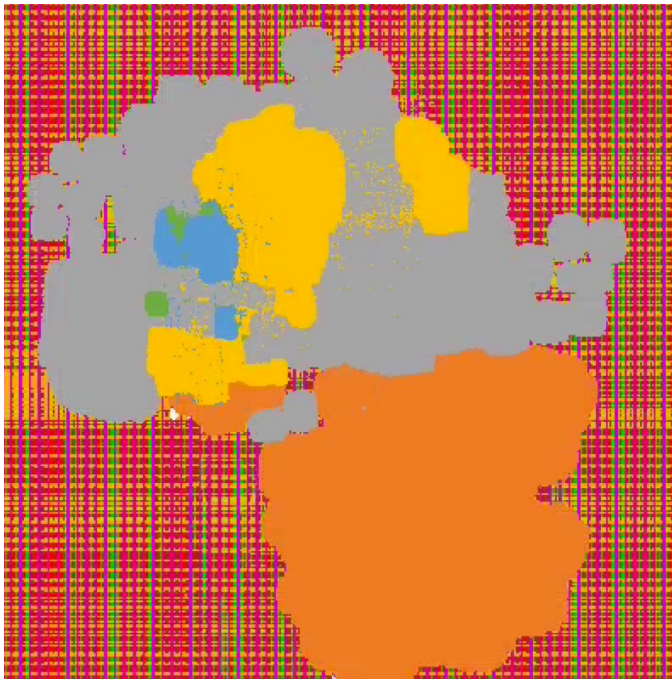
Training/Inference Cost

What we propose



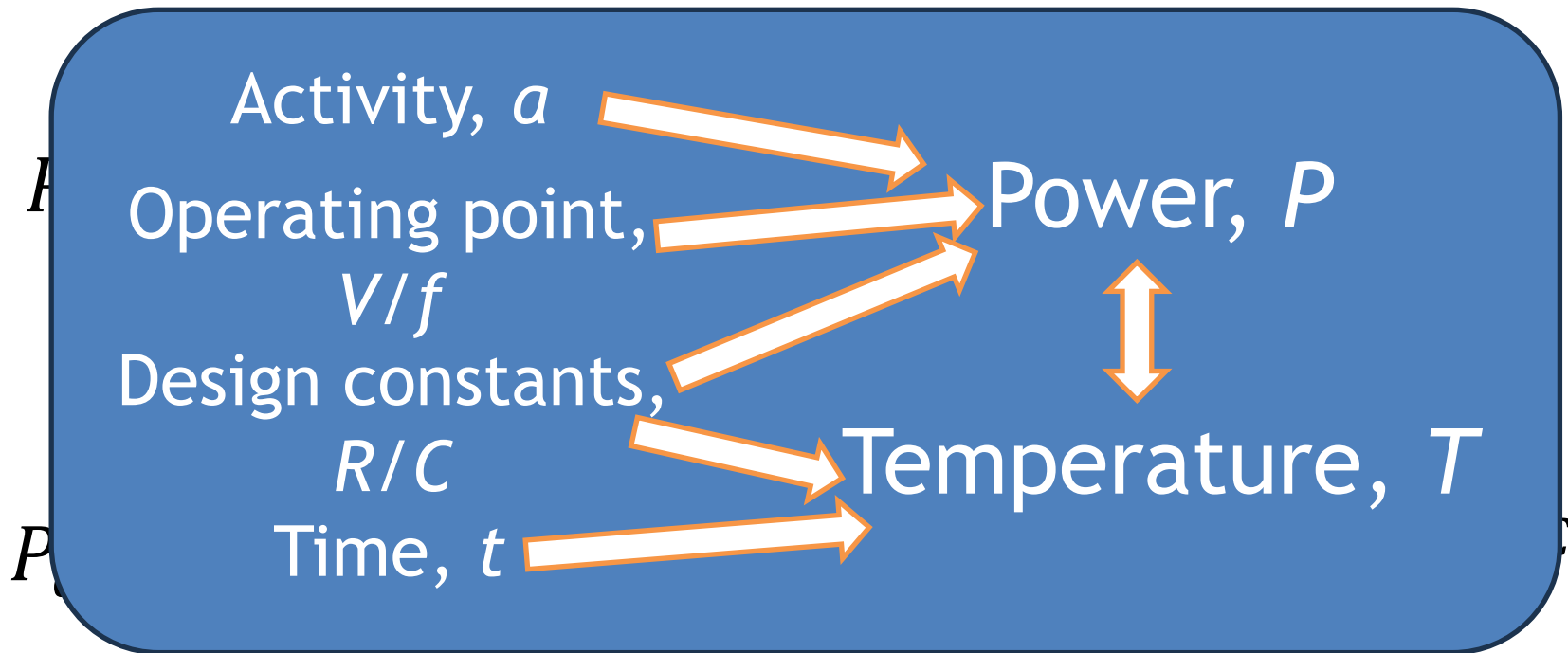
Myth - event selection

Workload-based correlation makes coarse abstractions without semantics

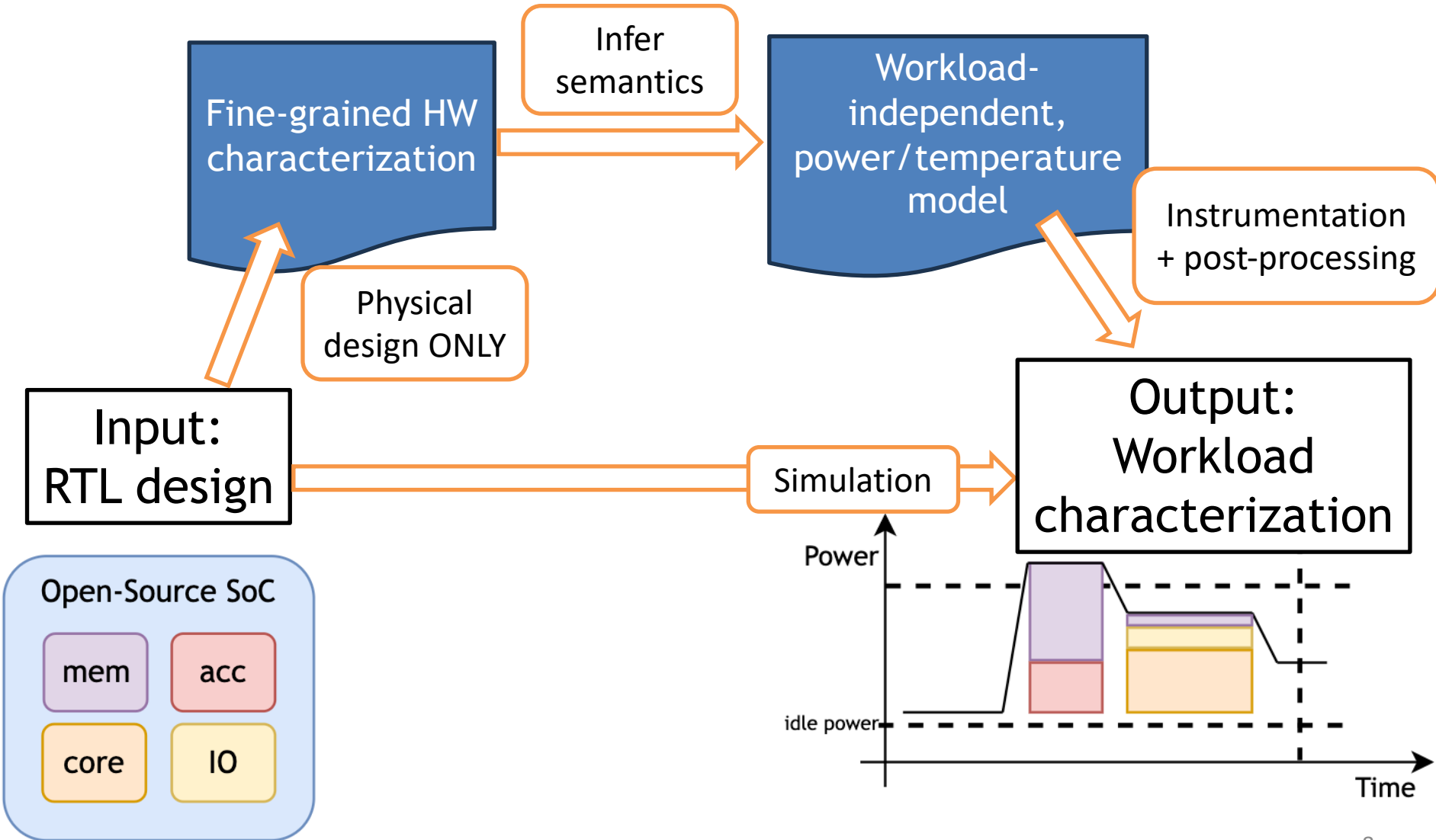


Myth - model complexity

“Representative” workloads miss access patterns influence on power

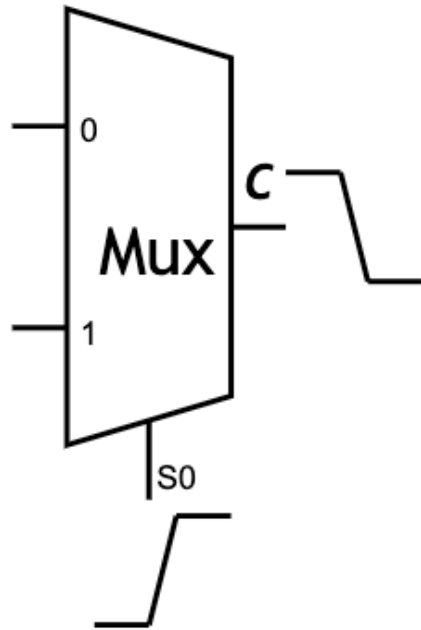


Contributions



Model - power

Per-gate activity

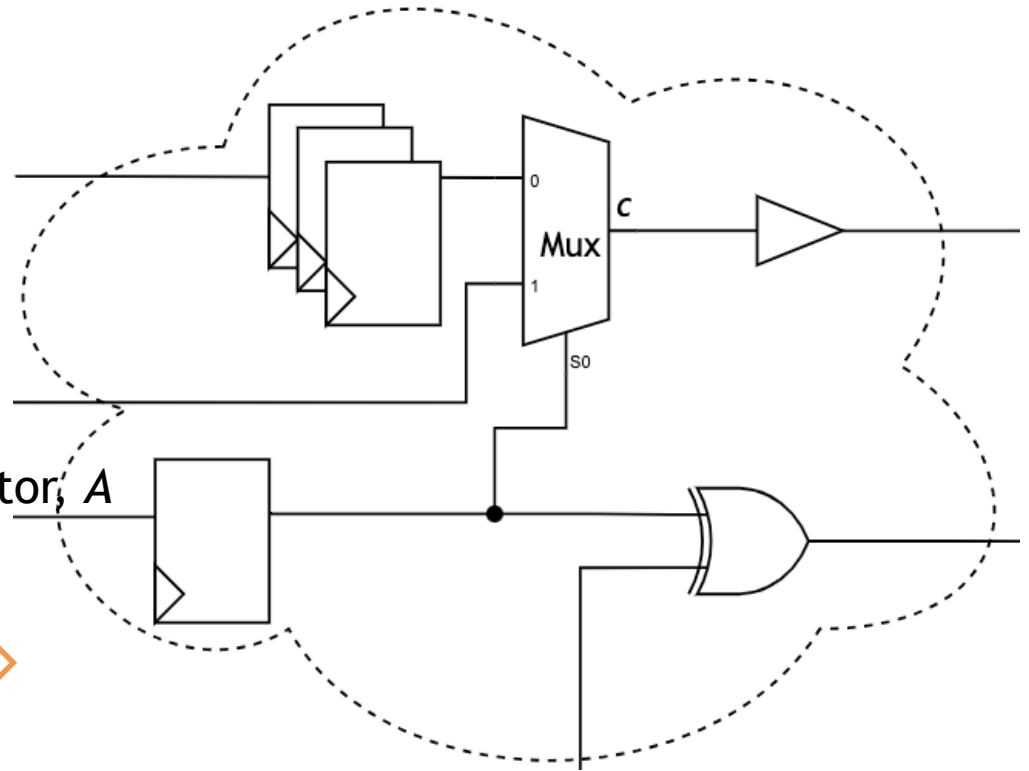


P_c

$\Pr(c|s_0)$

Composes into

Indicator, A



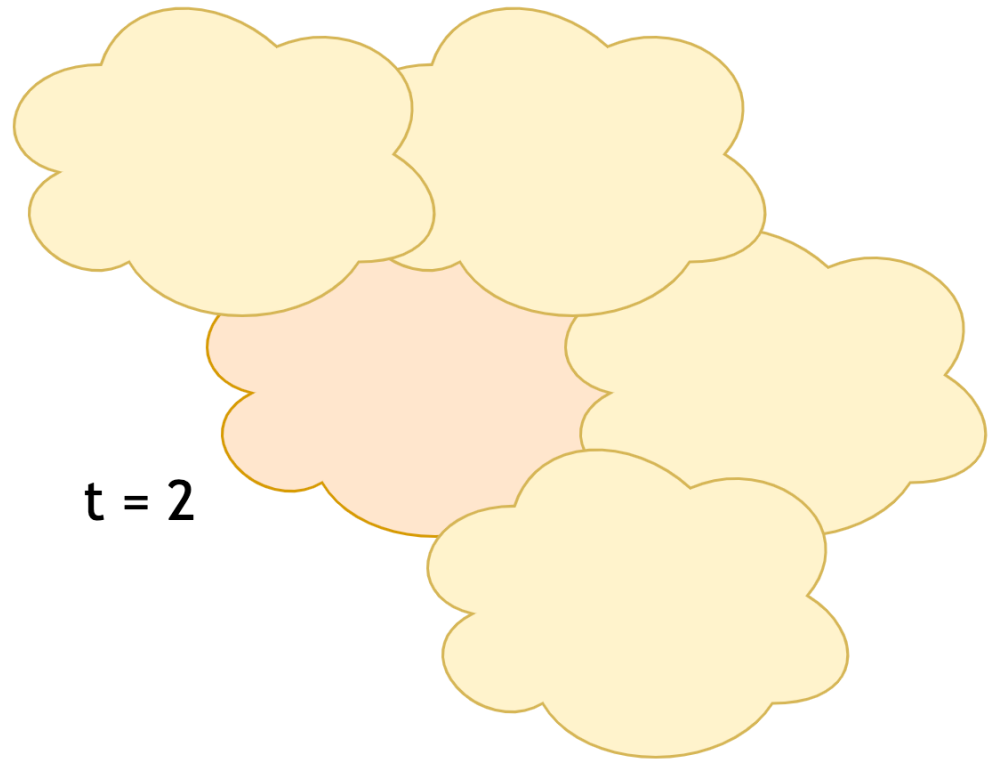
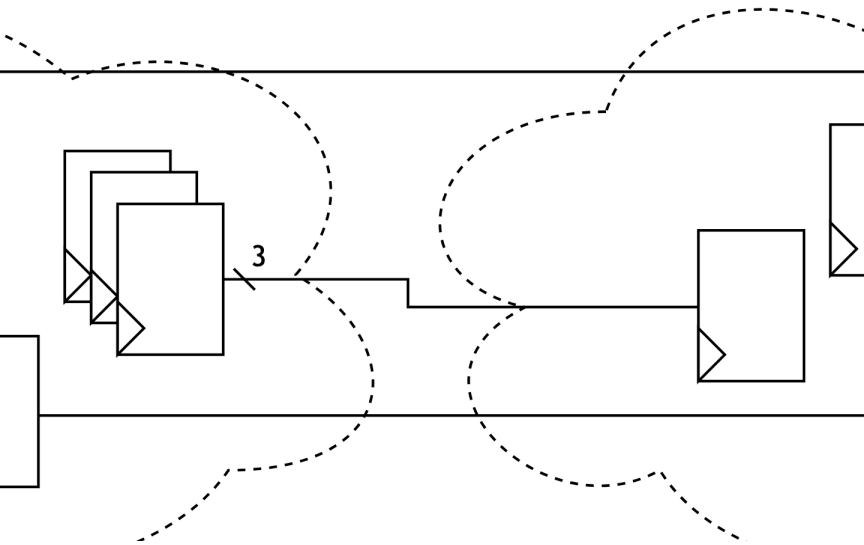
Weighted "atomic" power

$$P = P_A * 1 + P_c * \Pr(c|s_0) + \dots$$

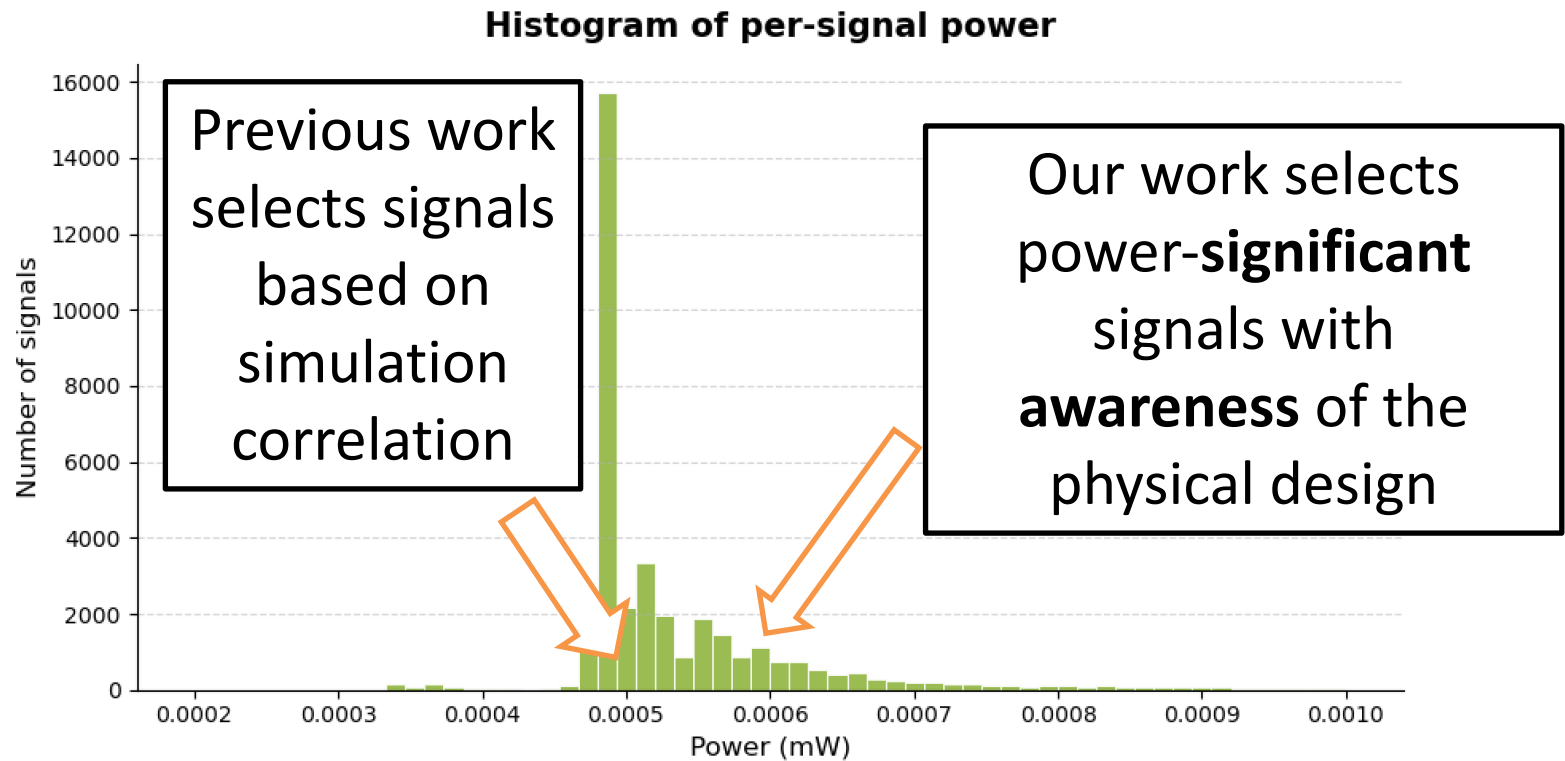
Model - temperature

Degree of connection governs thermal transfer.

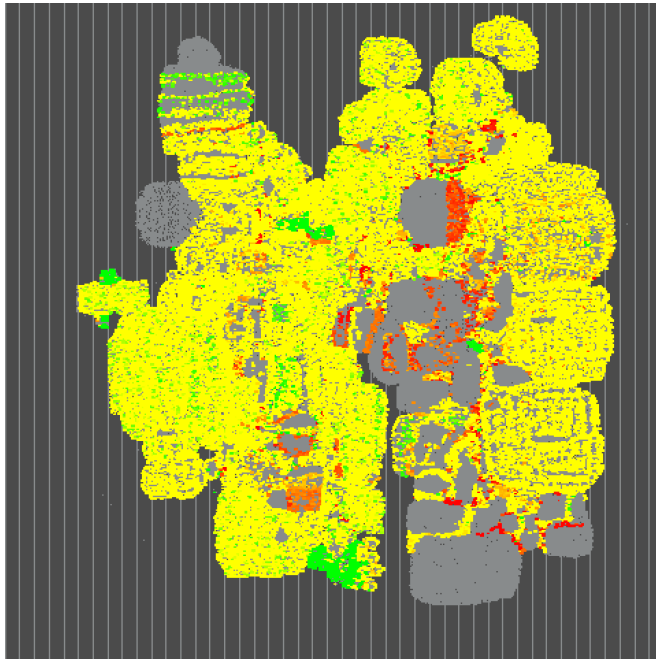
e.g., Degree = 5



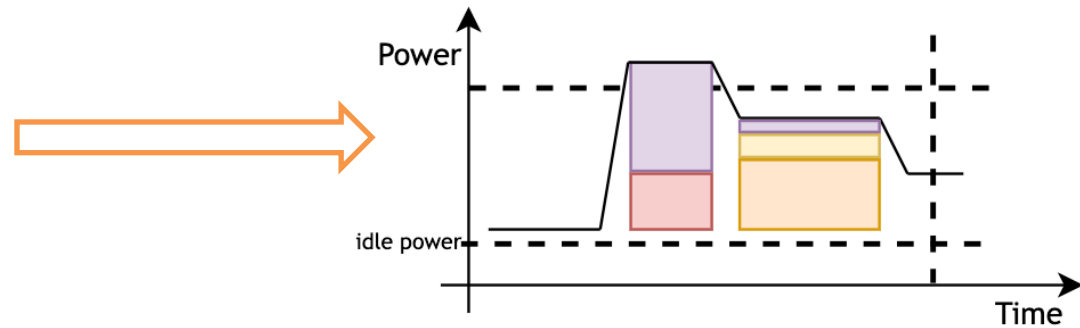
Results - Validation



Hypothesis: our clustering provides a better abstraction of the power distribution



**Windfoil: Workload-independent,
physical design-aware,
power and temperature model**



Open to discussion!

- Smarter clustering
- Stronger temperature model

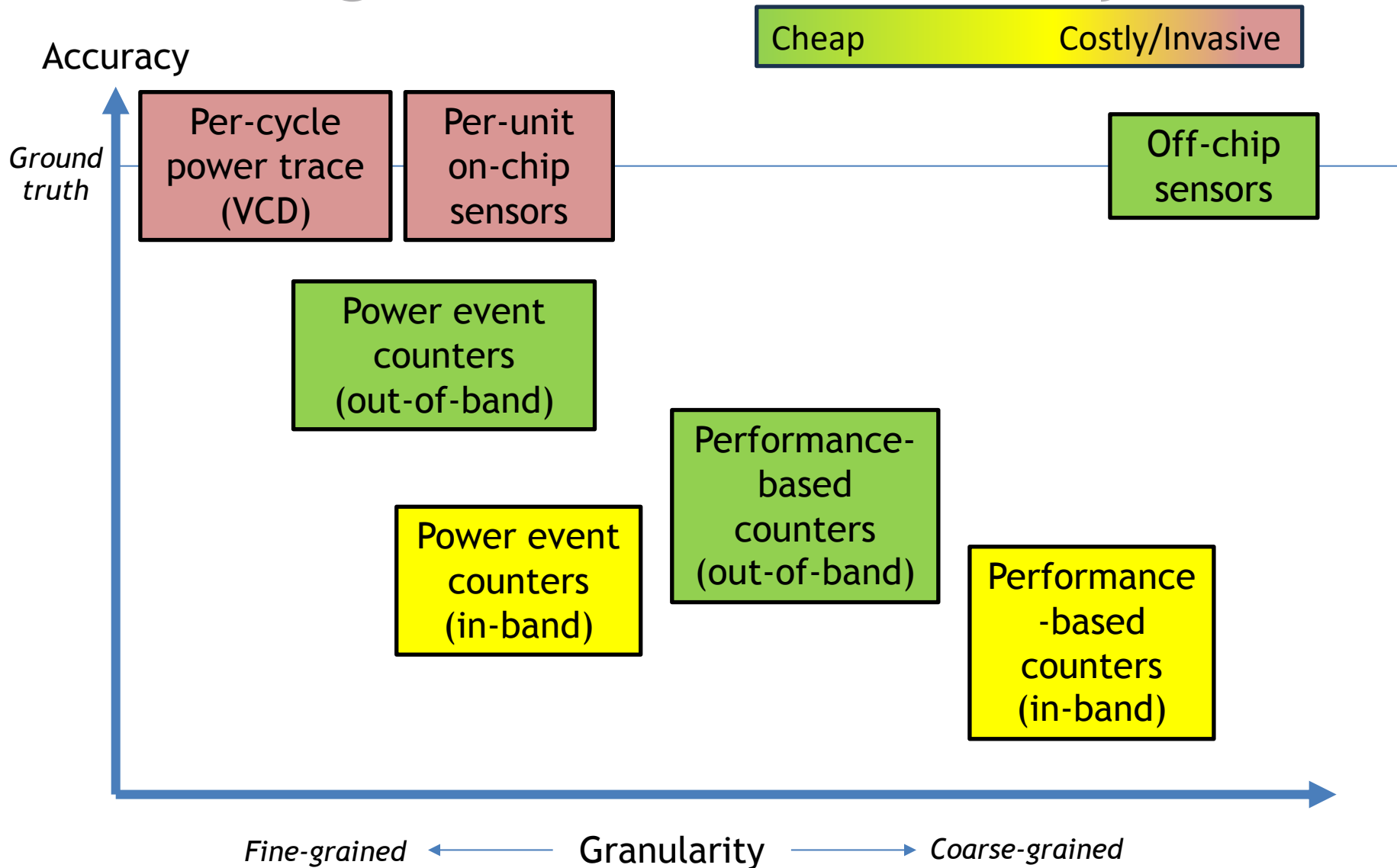
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Github: [michaelg29.github.io](https://github.com/michaelg29)



Backup slides

Challenge - model accuracy/cost

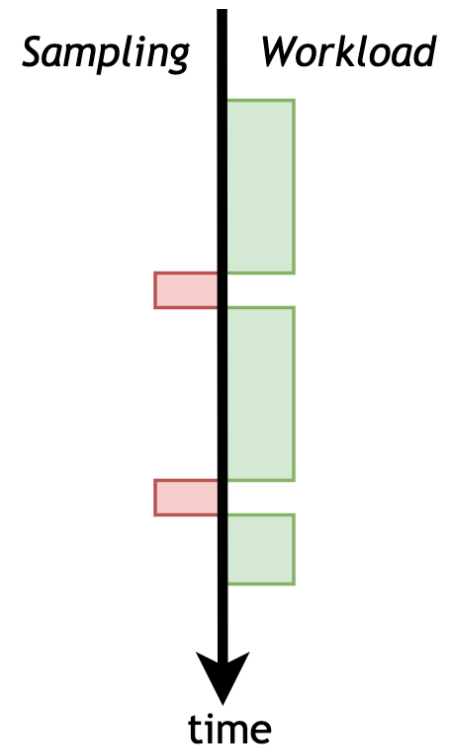
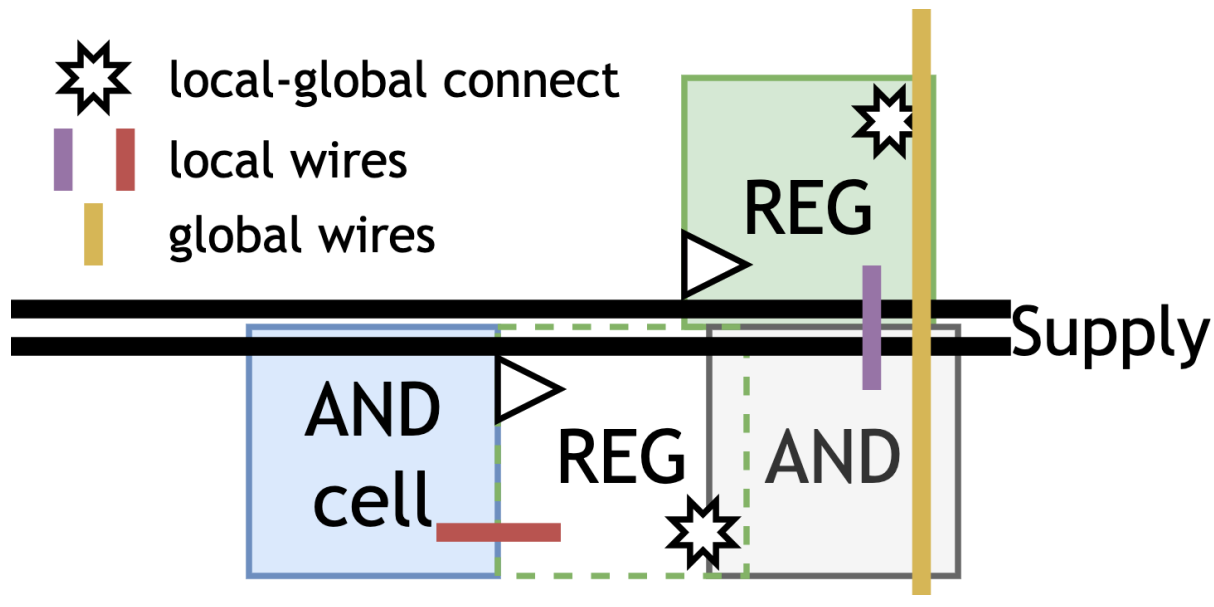


Myth - instrumentation

Dependence on instrumentation

- “Sensors do not affect results”
- “Sampling rate is non-significant”

=> Heisenbugs



Myth - model training

Dependence on training workload

- “Targeted microbenchmarks”
- “Siloed events”
- “Representative”
- “No interference”

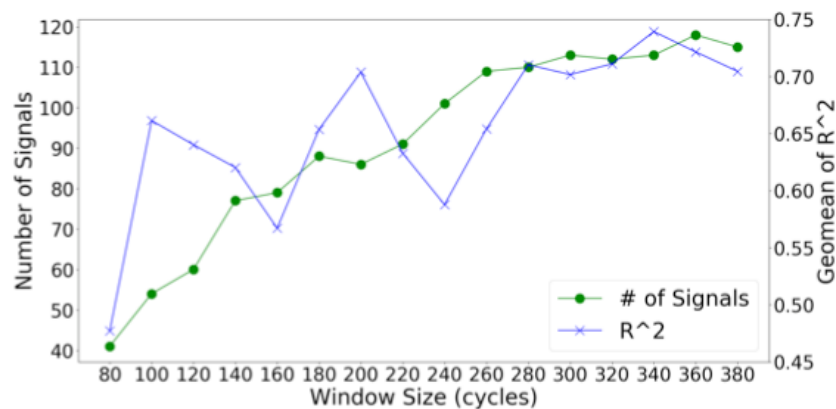


Figure 7: The number of selected signals and the geometric mean of R^2 across module-level power models for different window sizes

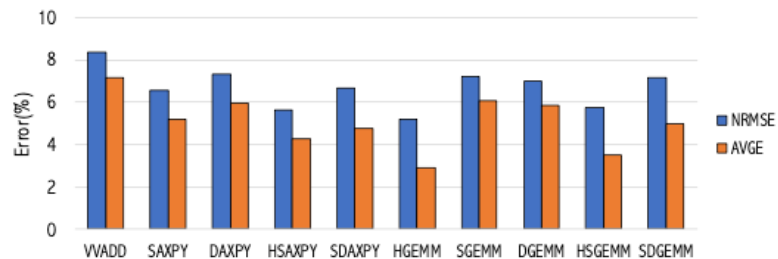


Figure 8: Power prediction errors for microbenchmarks

Kim et al., MICRO'19

Implementation

